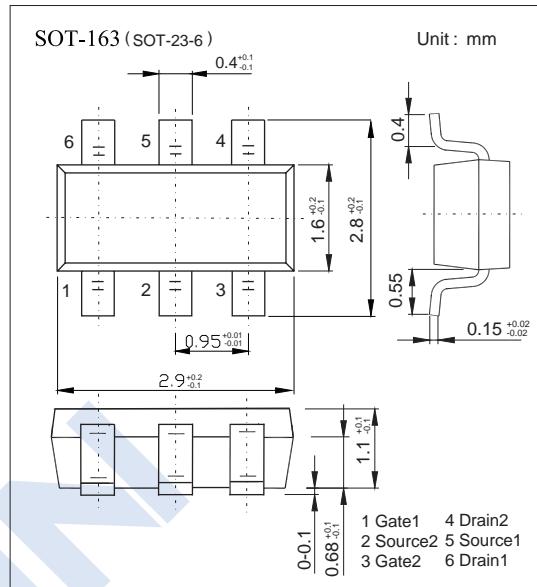
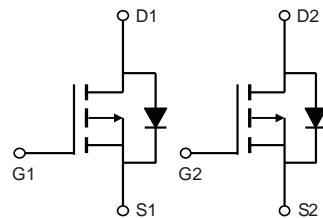


## Dual P-channel MOSFET

2KJ6027

## ■ Features

- $V_{DS}$  (V) = -20V
- $I_D$  = -4.0A
- Low threshold voltage
- Very fast switching
- Trench MOSFET technology

■ Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	
Continuous Drain Current, $t \leq 5\text{ s}$ *1	$I_D$	-4.0	A
Pulsed Drain Current ( $t_p \leq 10\mu\text{s}$ )	$I_{DM}$	-12	
Power Dissipation *1 *2	$P_D$	1210	mW
		515	
Thermal Resistance, Junction- to-Ambient in free air *2 in free air *1 in free air; $t \leq 5\text{ s}$ *1	$R_{\theta JA}$	244	$^\circ\text{C}/\text{W}$
		104	
		64	
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Junction Storage Temperature Range	$T_{Stg}$	-55 to 150	

\*1. Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6  $\text{cm}^2$ .

\*2. Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## Dual P-channel MOSFET

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■ Electrical Characteristics ( $T_A = 25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D=-250\mu\text{A}, V_{GS}=0\text{V}$	-20			V
Zero Gate Voltage Drain Current	$I_{DS(on)}$	$V_{DS}=-20\text{V}, V_{GS}=0\text{V}$			-1	$\mu\text{A}$
		$V_{DS}=-20\text{V}, V_{GS}=0\text{V}, T_J = 150^\circ\text{C}$			-10	
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 10\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-0.45		-0.95	V
Static Drain-Source On-Resistance (Note 1)	$R_{DS(on)}$	$V_{GS}=-4.5\text{V}, I_D=-2\text{A}$			80	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}, I_D=-1.5\text{A}$			100	
Forward Transconductance (Note 1)	$g_{FS}$	$V_{DS}=-10\text{V}, I_D=-2\text{A}$		9		S
Input Capacitance	$C_{iss}$	$V_{GS}=0\text{V}, V_{DS}=-10\text{V}, f=1\text{MHz}$		804		$\text{pF}$
Output Capacitance	$C_{oss}$			95		
Reverse Transfer Capacitance	$C_{rss}$			66		
Total Gate Charge	$Q_g$	$V_{DS}=-10\text{V}, I_D=-2\text{A}, V_{GS} = -4.5\text{V}$		6.3	9.5	$\text{nC}$
Gate Source Charge	$Q_{gs}$			1.2		
Gate Drain Charge	$Q_{gd}$			0.9		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-10\text{V}, I_D=-2\text{A}, V_{GS} = -4.5\text{V}$		7		$\text{ns}$
Turn-On Rise Time	$t_r$			15		
Turn-Off Delay Time	$t_{d(off)}$			41		
Turn-Off Fall Time	$t_f$			14		
Maximum Body-Diode Continuous Current	$I_S$				-1.3	A
Diode Forward Voltage	$V_{SD}$	$I_{SD}=-0.5 \text{ A}, V_{GS}=0\text{V}$			-1.2	V

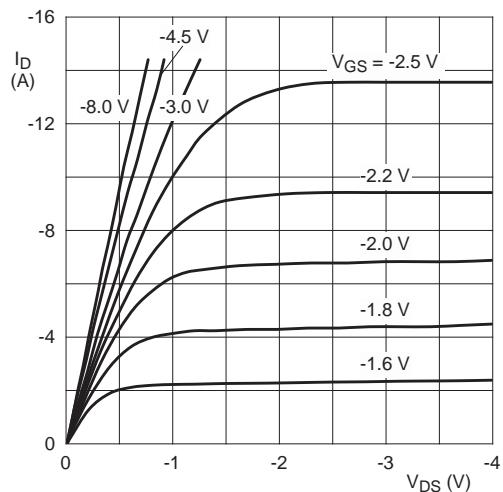
## ■ Marking

Marking	JAP
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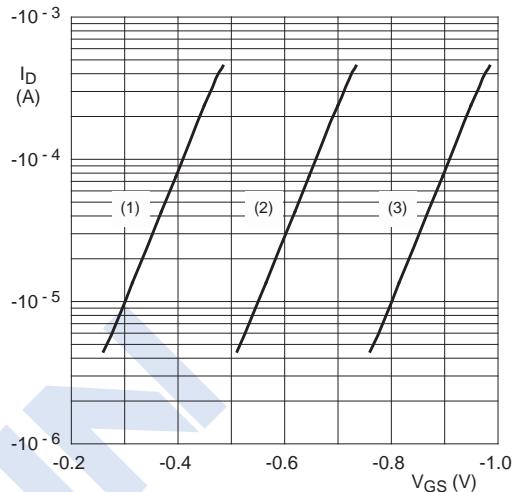
## Dual P-channel MOSFET

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## ■ Typical Characteristics

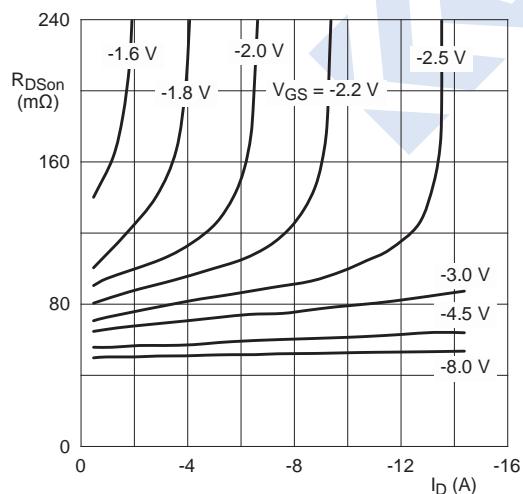
 $T_j = 25^\circ\text{C}$ 

**Fig. 1. Output characteristics: drain current as a function of drain-source voltage; typical values**

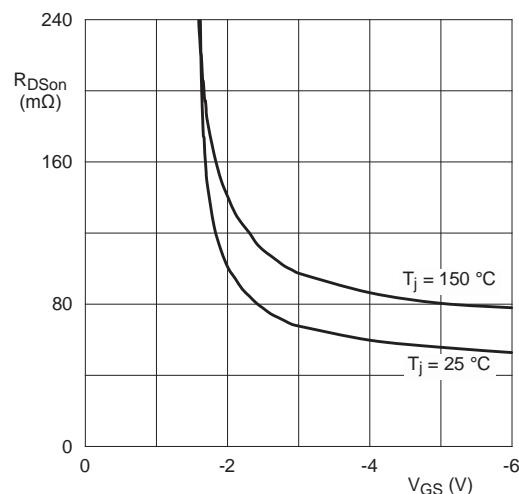
 $T_j = 25^\circ\text{C}; V_{DS} = -3\text{ V}$ 

- (1) minimum values
- (2) typical values
- (3) maximum values

**Fig. 2. Sub-threshold drain current as a function of gate-source voltage**

 $T_j = 25^\circ\text{C}$ 

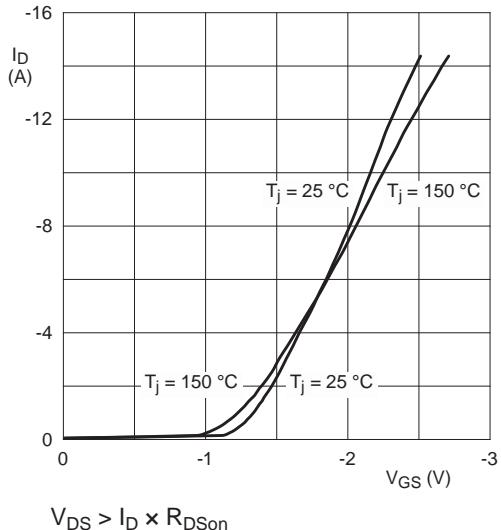
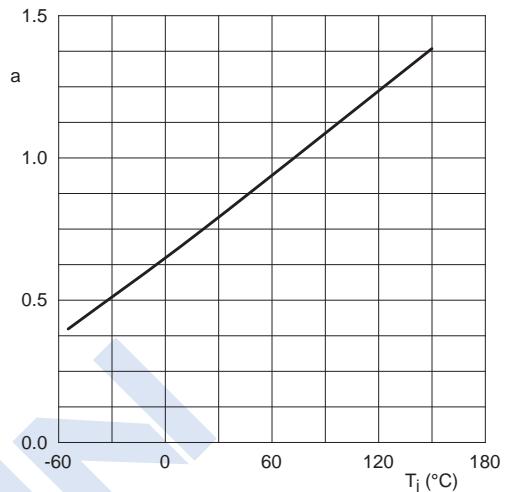
**Fig. 3. Drain-source on-state resistance as a function of drain current; typical values**

 $I_D = -2\text{ A}$ 

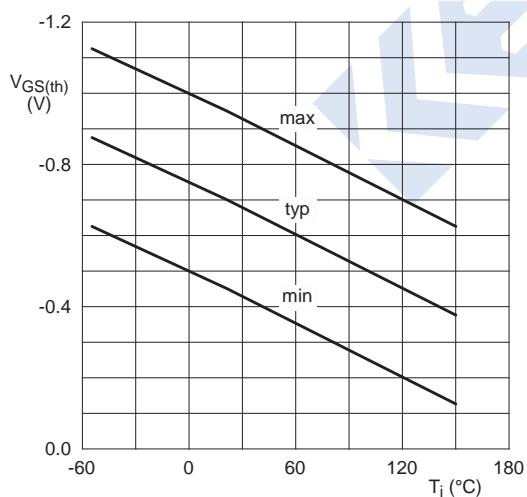
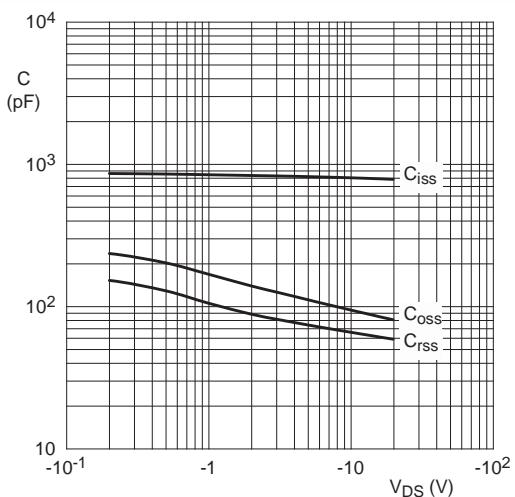
**Fig. 4. Drain-source on-state resistance as a function of gate-source voltage; typical values**

## Dual P-channel MOSFET

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 $V_{DS} > I_D \times R_{DSon}$ **Fig. 5. Transfer characteristics: drain current as a function of gate-source voltage; typical values****Fig. 6. Normalized drain-source on-state resistance as a function of junction temperature; typical values**

$$a = \frac{R_{DSon}}{R_{DSon}(25^\circ\text{C})}$$

 $I_D = -0.25 \text{ mA}; V_{DS} = V_{GS}$ **Fig. 7. Gate-source threshold voltage as a function of junction temperature** $f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}$ **Fig. 8. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values**