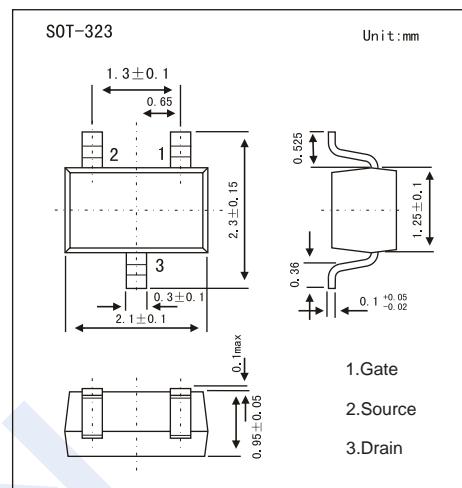
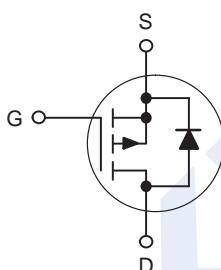


## P-Channel MOSFET

## 2KJ6040

## ■ Features

- $V_{DS}$  -20V
- $I_D$  (at  $V_{GS} = -4.5V$ ) -3.2A (Note 3)
- $R_{DS(ON)}$  (at  $V_{GS} = -4.5V$ ) 83mΩ (Typ.)
- $R_{DS(ON)}$  (at  $V_{GS} = -3.6V$ ) 88mΩ (Typ.)
- $R_{DS(ON)}$  (at  $V_{GS} = -2.5V$ ) 104mΩ (Typ.)

■ Absolute Maximum Ratings ( $T_J = 25^\circ\text{C}$  unless otherwise noted.)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DS}$	-20	V
Gate-Source Voltage		$V_{GS}$	$\pm 8$	
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	-3.2	A
	$T_C = 70^\circ\text{C}$		-2.2	
	$T_A = 25^\circ\text{C}$		-1.4 (Note 1,2)	
	$T_A = 70^\circ\text{C}$		-1.1 (Note 1,2)	
Pulsed Drain Current	$t_p = 10\mu\text{s}$	$I_{DM}$	-9	W
Source Current (Body Diode), Continuous		$I_S$	-0.5	
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	0.5	
	$T_C = 70^\circ\text{C}$		0.3	
	$T_A = 25^\circ\text{C}$		0.4 (Note 1,2)	
	$T_A = 70^\circ\text{C}$		0.3 (Note 1,2)	
Thermal Resistance, Junction- to-Ambient (Note 1)		$R_{\theta JA}$	300	$^\circ\text{C}/\text{W}$
Junction Temperature		$T_J$	150	$^\circ\text{C}$
Storage Temperature Range		$T_{stg}$	-55 to 150	

Notes:

1. Surface mounted on 1" x 1" FR4 board.

2.  $t = 10 \mu\text{s}$ .3. Based on  $T_C = 25^\circ\text{C}$ .

## P-Channel MOSFET

## 2KJ6040

■ Electrical Characteristics ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

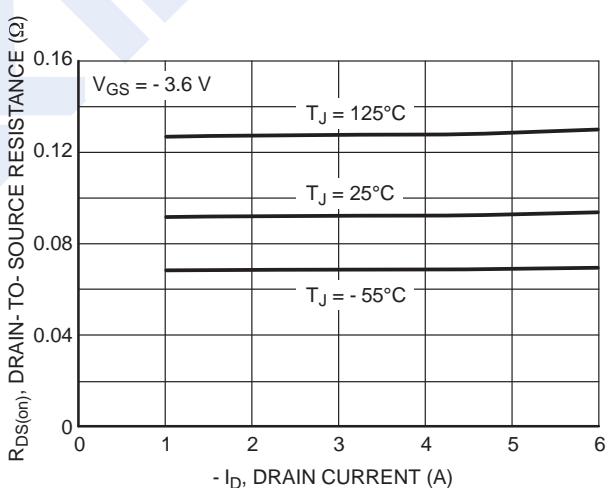
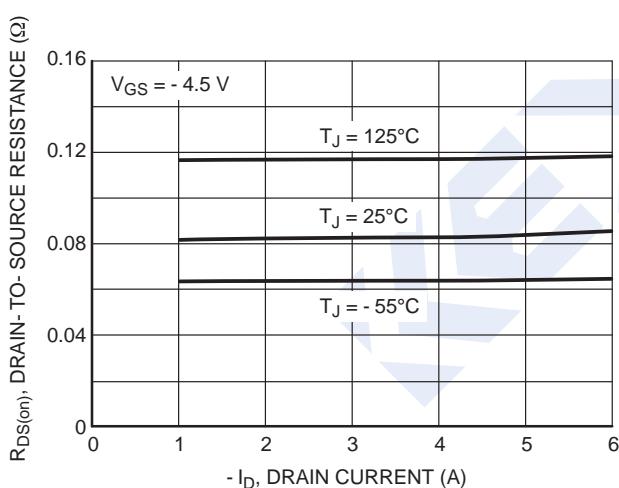
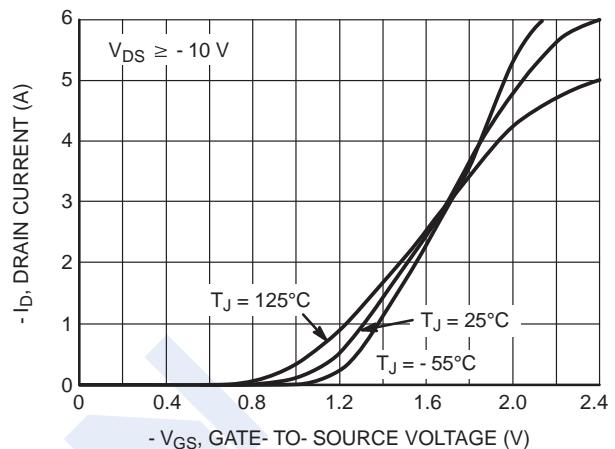
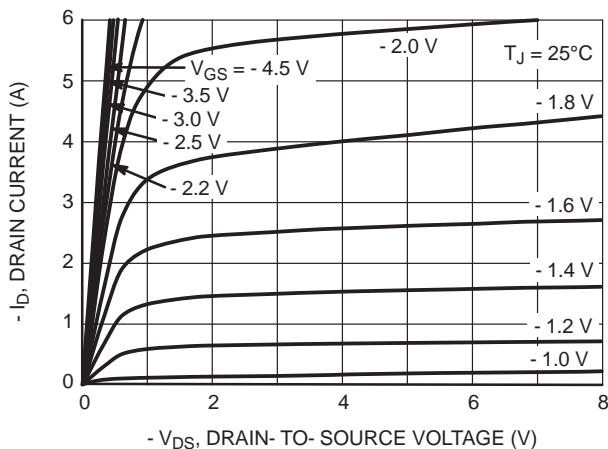
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{I}_D = -250\mu\text{A}, \text{V}_{\text{GS}} = 0\text{V}$	-20			V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}} = -16\text{V}, \text{V}_{\text{GS}} = 0\text{V}$			-1	$\mu\text{A}$
		$\text{V}_{\text{DS}} = -16\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{T}_J = 70^\circ\text{C}$			-5	
Gate-Body Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{DS}} = 0\text{V}, \text{V}_{\text{GS}} = \pm 8\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS (Note 4)</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = -250\mu\text{A}$	-0.45		-1.5	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}} = -4.5\text{V}, \text{I}_D = -1.4\text{A}$		83	120	$\text{m}\Omega$
		$\text{V}_{\text{GS}} = -3.6\text{V}, \text{I}_D = -1.3\text{A}$		88	130	
		$\text{V}_{\text{GS}} = -2.5\text{V}, \text{I}_D = -1.2\text{A}$		104	160	
Forward Transconductance	$\text{g}_{\text{FS}}$	$\text{V}_{\text{DS}} = -5\text{V}, \text{I}_D = -1.3\text{A}$		5.2		S
<b>CHARGES AND CAPACITANCES</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = -20\text{V}, \text{f} = 1\text{MHz}$		603	840	$\text{pF}$
Output Capacitance	$\text{C}_{\text{oss}}$			90	125	
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$			62	85	
Total Gate Charge	$\text{Q}_{\text{g(TOT)}}$	$\text{V}_{\text{DS}} = -4.5\text{V}, \text{I}_D = -1.0\text{A}$ $\text{V}_{\text{GS}} = -4.5\text{V}$		6.4	9.0	$\text{nC}$
	$\text{Q}_{\text{g(TH)}}$			0.7		
Gate Source Charge	$\text{Q}_{\text{gs}}$			1.0		
Gate Drain Charge	$\text{Q}_{\text{gd}}$			1.5		
<b>SWITCHING CHARACTERISTICS (Note 5)</b>						
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{GS}} = -4.5\text{V}, \text{V}_{\text{DD}} = -4.0\text{V}, \text{I}_D = 1.0\text{A}, \text{R}_G = 6.2\Omega$		6.2	12	$\text{ns}$
Turn-On Rise Time	$\text{t}_r$			14.9	25	
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$			26	40	
Turn-Off Fall Time	$\text{t}_f$			18	30	
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
Body Diode Reverse Recovery Time	$\text{t}_{\text{rr}}$	$\text{I}_{\text{s}} = -1.0\text{A}, \text{V}_{\text{GS}} = 0\text{V}, \text{dI/dt} = 100\text{A}/\mu\text{s}$		10.9	20	$\text{ns}$
Body Diode Reverse Recovery Charge	$\text{Q}_{\text{rr}}$			4.25		
Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{I}_{\text{s}} = -0.3\text{ A}, \text{V}_{\text{GS}} = 0\text{V}$			-1.2	V

Notes:

4. Pulse Test: pulse width  $\leqslant 300\mu\text{s}$ , duty cycle  $\leqslant 2\%$ .
5. Switching characteristics are independent of operating junction temperatures.

## ■ Marking

Marking	1B
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**P-Channel MOSFET****2KJ6040****■ Typical Characteristics**

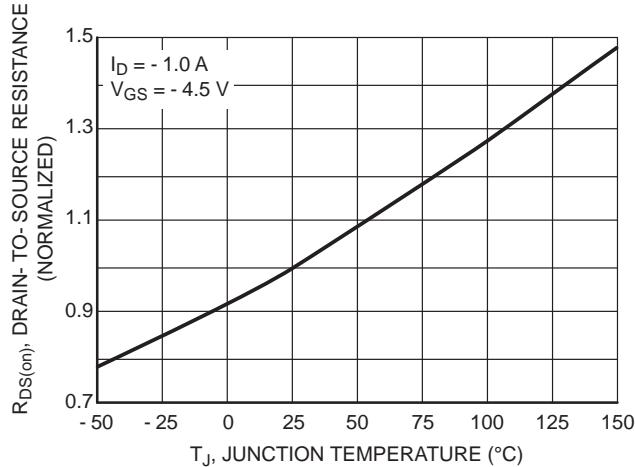
**P-Channel MOSFET****2KJ6040**

Figure 5. On - Resistance Variation with Temperature

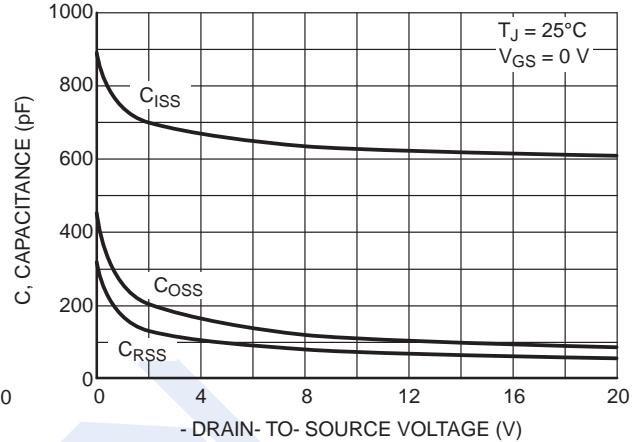


Figure 6. Capacitance Variation

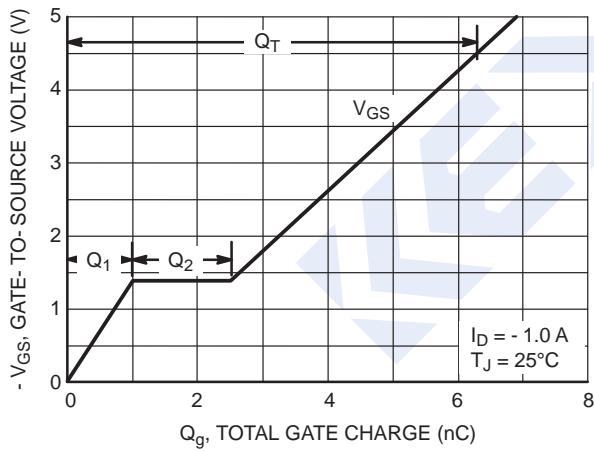


Figure 7. Gate - to - Source and Drain - to - Source Voltage versus Total Charge

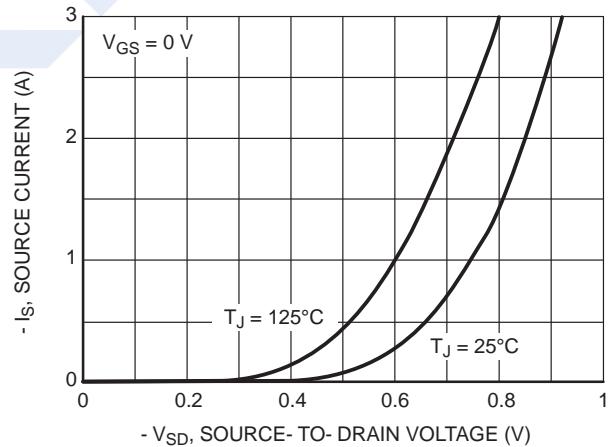


Figure 8. Diode Forward Voltage versus Current