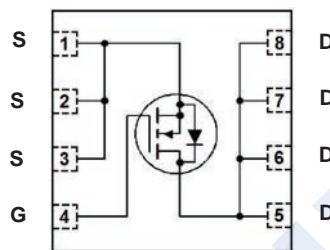


N-Channel MOSFET

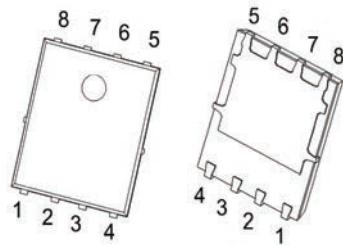
2KK5120DFN

■ Features

- V_{DS} (V) = 40 V
- I_D = 150 A
- $R_{DS(ON)}$ (at V_{GS} = 10 V) < 1.8 mΩ



PDFN5x6-8

■ Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	150	A
$T_c = 100^\circ\text{C}$		100	
Pulsed Drain Current (Note 1)	I_{DM}	560	
Power Dissipation	P_D	92.6	W
Power Dissipation – Derate above 25°C		0.74	W/°C
Single Pulse Avalanche Energy (Note 2)	E_{AS}	450	mJ
Single Pulse Avalanche Current (Note 2)	I_{AS}	30	A
Thermal Resistance, Junction- to-Ambient	$R_{\theta JA}$	62	°C/W
Thermal Resistance, Junction- to-Case	$R_{\theta JC}$	1.35	
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{stg}	-55 to 150	

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. EAS condition : $T_j=25^\circ\text{C}$, $V_{DD}=25\text{V}$, $V_G=10\text{V}$, $L=1\text{mH}$, $I_{AS}=30\text{A}$

N-Channel MOSFET**2KK5120DFN****■ Electrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250 \mu\text{A}, V_{GS} = 0\text{V}$	40			V
Zero Gate Voltage Drain Current	$I_{DS(on)}$	$V_{DS} = 40\text{V}, V_{GS} = 0\text{V}$			1	μA
		$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Gate to Source Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
On Characteristics (Note 1)						
Gate to Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.0		2.0	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}, I_D = 50\text{A}$			1.8	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 25\text{A}$			2.4	
Forward Transconductance	g_{FS}	$V_{DS} = 10\text{V}, I_D = 50\text{A}$		120		S
Dynamic Characteristics (Note 1)						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = 20\text{V}, f = 1\text{MHz}$		2500		pF
Output Capacitance	C_{oss}			230		
Reverse Transfer Capacitance	C_{rss}			3.2		
Gate Resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, F=1\text{MHz}$		2.0	4.8	Ω
Switching Characteristics (Note 1)						
Total Gate Charge	Q_g	$V_{GS} = 10\text{V}, V_{DS} = 20\text{V}, I_D = 50\text{A}$		75		nC
Gate Source Charge	Q_{gs}			24		
Gate Drain Charge	Q_{gd}			19		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DD} = 20\text{V}, I_D = 50\text{A}, R_G = 3\Omega$		20		ns
Turn-On Rise Time	t_r			32		
Turn-Off Delay Time	$t_{d(off)}$			98		
Turn-Off Fall Time	t_f			32		
Drain-Source Diode Characteristics						
Body Diode Reverse Recovery Time	t_{rr}	$I_S = 50\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		64		ns
Body Diode Reverse Recovery Charge	Q_{rr}			98		nC
Maximum Body-Diode Continuous Current	I_S	$V_G = V_D = 0\text{V}$, Force Current			200	A
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}, I_S = 1\text{A}$			1	V

Notes:

1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

■ Marking

Marking	K5120 KC***
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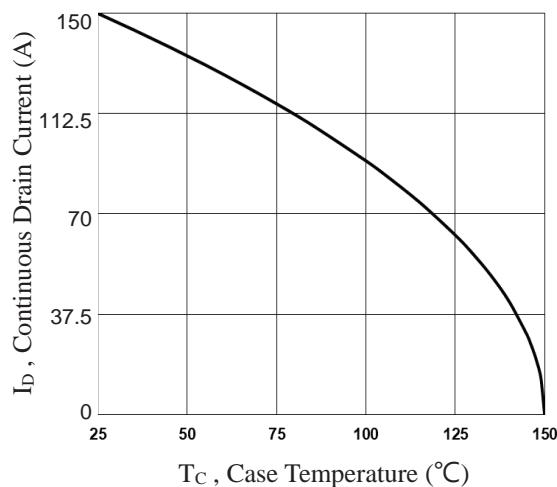
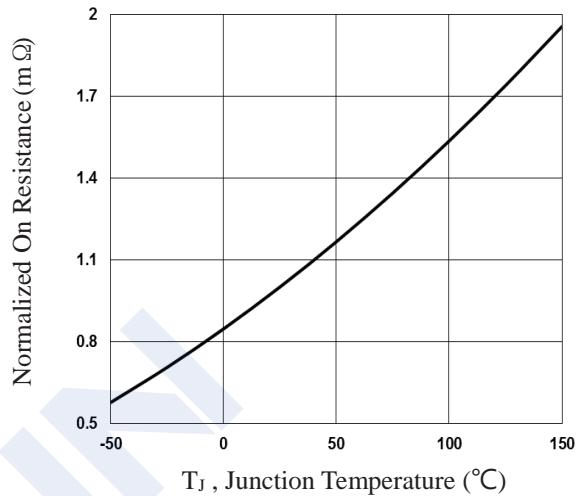
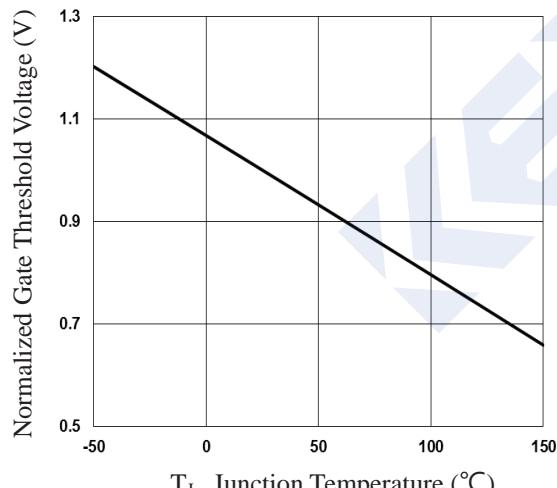
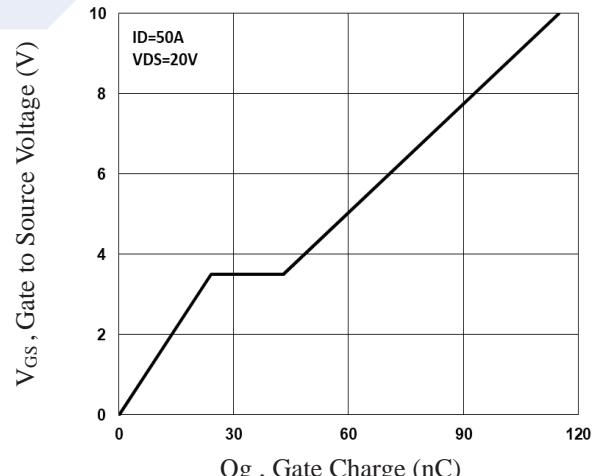
N-Channel MOSFET**2KK5120DFN****■ Typical Electrical Characteristics**Fig.1 Continuous Drain Current vs. T_C Fig.2 Normalized RDS(on) vs. T_J Fig.3 Normalized V_{GS} vs. T_J 

Fig.4 Gate Charge Characteristics

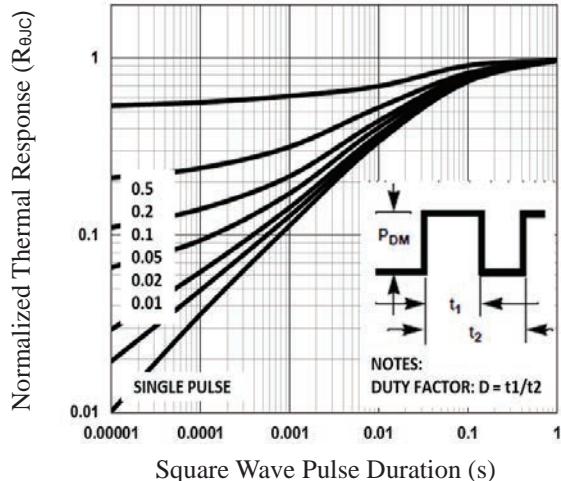
N-Channel MOSFET**2KK5120DFN**

Fig.5 Normalized Transient Impedance

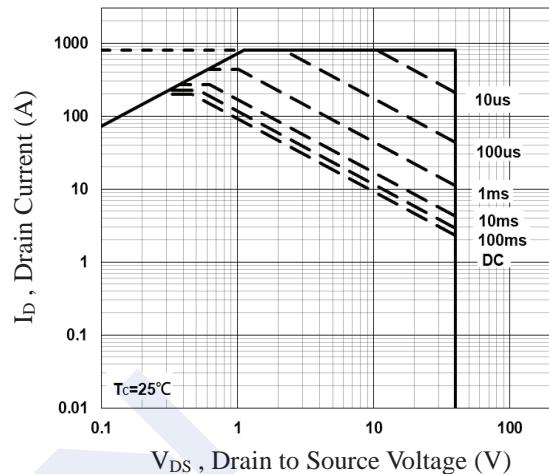


Fig.6 Maximum Safe Operation Area

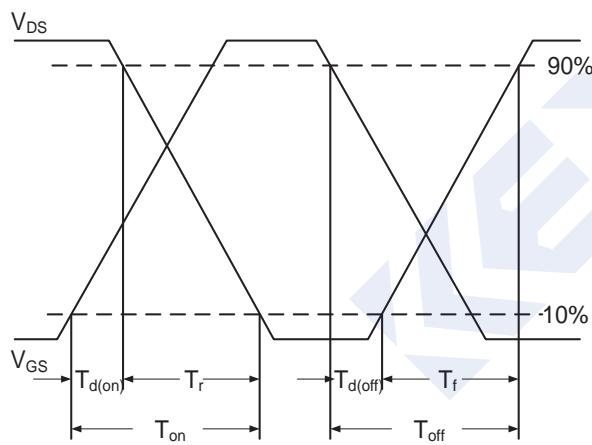


Fig.7 Switching Time Waveform

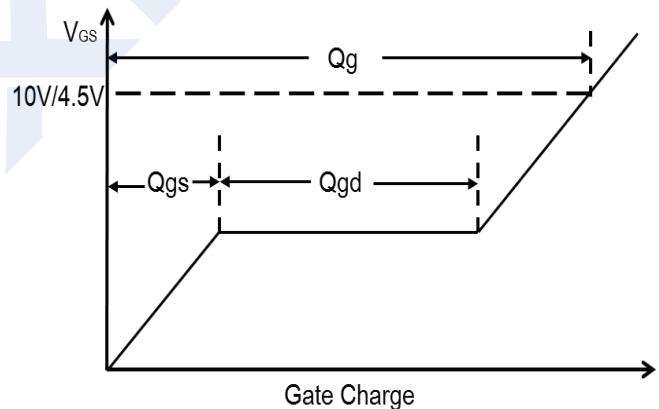
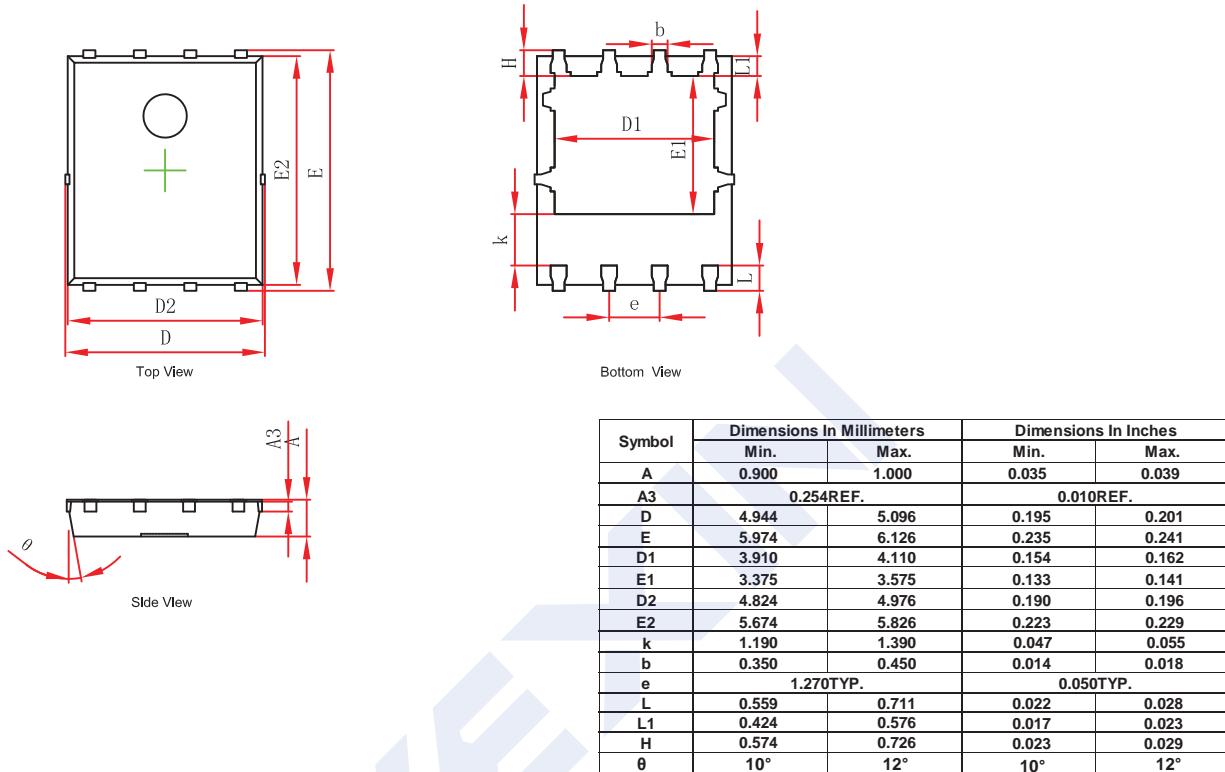
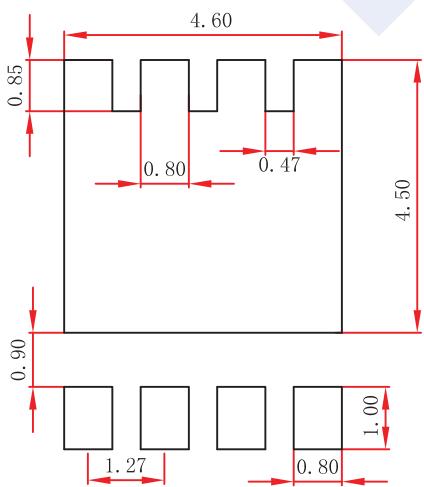


Fig.8 Gate Charge Waveform

N-Channel MOSFET**2KK5120DFN****■ PDFN5x6-8 Package Outline Dimensions****■ PDFN5x6-8 Suggested Pad Layout**

Note:
 1. Controlling dimension: in millimeters.
 2. General tolerance: $\pm 0.05\text{mm}$.
 3. The pad layout is for reference purposes only.