MOS Field Effect Transistor

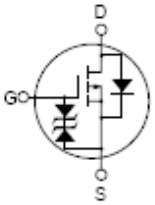
2SJ598

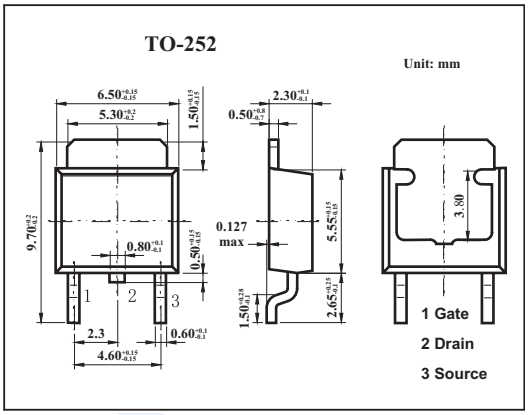
■ Features

- Low on-resistance

  $R_{DS(on)1}$ = 130 mΩ MAX. (V_{GS} =-10 V, I_D = -6 A)

  $R_{DS(on)2}$ = 190 mΩ MAX. (V_{GS} = -4.0 V, I_D =-6 A)
- Low C_{iss}: C_{iss} = 720 pF TYP.
- Built-in gate protection diode





■ Absolute Maximum Ratings Ta = 25℃

Parameter	Symbol	Rating	Unit
Drain to source voltage	V_{DSS}	-60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current (DC)	I_D	±12	A
Drain current(pulse) *	I_D	±30	A
Power dissipation	P_D	23	W
Channel temperature	T_{ch}	150	℃
Storage temperature	T_{stg}	-55 to +150	℃

* PW≤ 10 μ s, duty cycle ≤ 1 %

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■ Electrical Characteristics Ta = 25℃

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	I_{DSS}	V_{DS}=-60V,V_{GS}=0			-10	μA
Gate leakage current	I_{GSS}	V_{GS}=±16V,V_{DS}=0			±10	μA
Gate to source cutoff voltage	$V_{GS(off)}$	V_{DS}=-10V,I_D=-1mA	-1.5	-2.0	-2.5	V
Forward transfer admittance	$\|Y_{fs}\|$	V_{DS}=-10V,I_D=-6A	5	11		S
Drain to source on-state resistance	$R_{DS(on)}$	V_{GS}=-10V,I_D=-6A		102	130	mΩ
		V_{GS}=-4.0V,I_D=-6A		131	190	mΩ
Input capacitance	C_{iss}	V_{DS}=-10V,V_{GS}=0,f=1MHZ		720		pF
Output capacitance	C_{oss}			150		pF
Reverse transfer capacitance	C_{rss}			50		pF
Turn-on delay time	$t_{d(on)}$	$V_{GS(on)}$=-10V,I_D=--6A ,V_{DD}=-30V,R_G=0 Ω		70		ns
Rise time	t_r			4		ns
Turn-off delay time	$t_{d(off)}$			35		ns
Fall time	t_f			10		ns
Total Gate Charge	Q_G	I_D = -12 A		15		nC
Gate to Source Charge	Q_{GS}	V_{DD}= -48 V		3		nC
Gate to Drain Charge	Q_{GD}	V_{GS} =-10 V		4		nC
Body Diode Forward Voltage	$V_{F(S-D)}$	I_F = 12 A, V_{GS} = 0 V		0.98		V
Reverse Recovery Time	t_{rr}	I_F = 12 A, V_{GS} = 0 V		50		ns
Reverse Recovery Charge	Q_{rr}	di/dt = 100 A / μs		100		nC