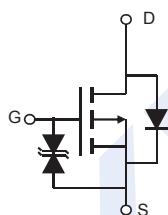
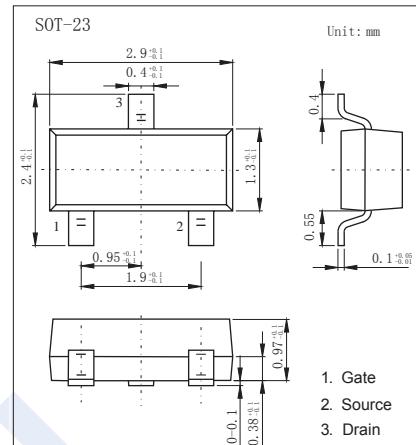


P-Channel MOSFET

KI015P

■ Features

- V_{DS} (V) = -20V
- I_D = -4A (V_{GS} = -4.5V)
- $R_{DS(ON)} = 50\text{m}\Omega$ ($V_{GS} = -4.5\text{V}$ Typ.)
- $R_{DS(ON)} = 70\text{m}\Omega$ ($V_{GS} = -2.5\text{V}$ Typ.)
- $R_{DS(ON)} = 110\text{m}\Omega$ ($V_{GS} = -1.8\text{V}$ Typ.)
- ESD Protected



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current $T_a = 25^\circ\text{C}$	I_D	-4	A
$T_a = 70^\circ\text{C}$	I_D	-3.5	
Pulsed Drain Current	I_{DM}	-30	
Power Dissipation (Note.1) $T_a = 25^\circ\text{C}$	P_D	1.5	W
$T_a = 70^\circ\text{C}$	P_D	1	
Thermal Resistance.Junction- to-Ambient $t \leq 10\text{s}$ Steady-State	R_{thJA}	80	$^\circ\text{C}/\text{W}$
	R_{thJA}	100	
Thermal Resistance.Junction- to-Lead	R_{thJL}	52	
Junction Temperature	T_J	150	
Junction Storage Temperature Range	T_{stg}	-55 to 150	$^\circ\text{C}$

Note.1: The power dissipation P_D is based on $T_{J(MAX)}=150^\circ\text{C}$, using $\leq 10\text{s}$ junction-to-ambient thermal resistance.

P-Channel MOSFET

KI015P

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =-250 μ A, V _{GS} =0V	-20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _D =-20V, V _{GS} =0V		-1		μ A
		V _D =-20V, V _{GS} =0V, T _J =55°C		-5		
Gate-Body leakage current	I _{GSS}	V _D =0V, V _{GS} =±10V		±10		uA
Gate Threshold Voltage	V _{GS(th)}	V _D =V _{GS} I _D =-250 μ A	-0.4		-1.0	V
Static Drain-Source On-Resistance	R _{D(on)}	V _{GS} =-4.5V, I _D =-4A		50	63	m Ω
		V _{GS} =-4.5V, I _D =-4A T _J =125°C			80	
		V _{GS} =-2.5V, I _D =-4A		70	91	
		V _{GS} =-1.8V, I _D =-2A		110	120	
		V _{GS} =-1.5V, I _D =-1A		118		
On state drain current	I _{D(on)}	V _{GS} =-4.5V, V _D =-5V	-30			A
Forward Transconductance	g _{FS}	V _D =-5V, I _D =-4A		20		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _D =-10V, f=1MHz		1450		pF
Output Capacitance	C _{oss}			205		
Reverse Transfer Capacitance	C _{rss}			160		
Gate resistance	R _G	V _{GS} =0V, V _D =0V, f=1MHz		6.5		Ω
Total Gate Charge	Q _g	V _{GS} =-4.5V, V _D =-10V, I _D =-4A		17.2		nC
Gate Source Charge	Q _{gs}			1.3		
Gate Drain Charge	Q _{gd}			4.5		
Turn-On Delay Time	t _{d(on)}	V _{GS} =-4.5V, V _D =-10V, R _L =2.5 Ω , R _{GEN} =3 Ω		9.5		ns
Turn-On Rise Time	t _r			17		
Turn-Off Delay Time	t _{d(off)}			94		
Turn-Off Fall Time	t _f			35		
Body Diode Reverse Recovery Time	t _{rr}	I _F =-4A, dI/dt=100A/ μ s		31		nC
Body Diode Reverse Recovery Charge	Q _{rr}			13.8		
Maximum Body-Diode Continuous Current	I _s				-2.2	A
Diode Forward Voltage	V _{SD}	I _s =-1A, V _{GS} =0V			-1.2	V

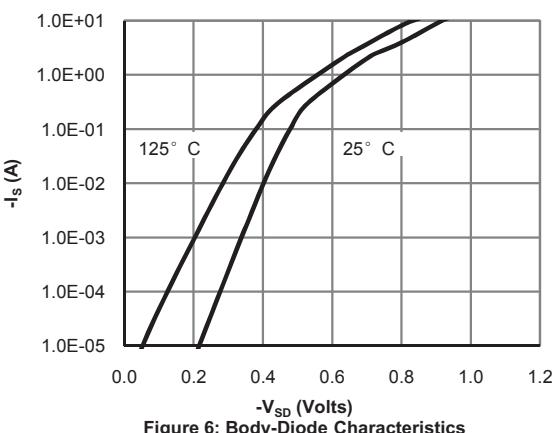
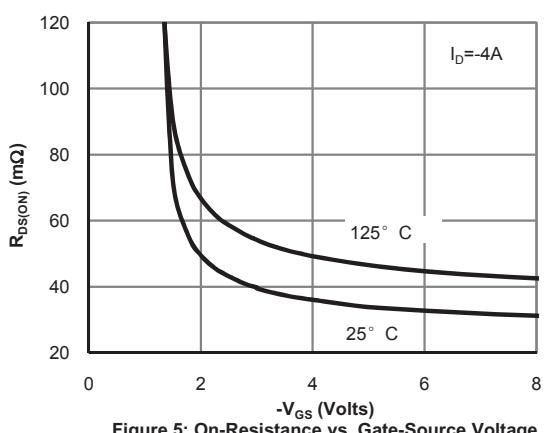
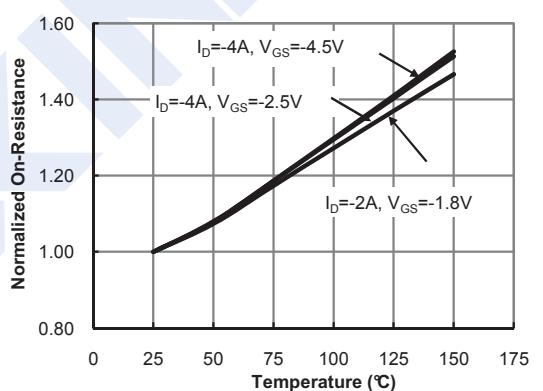
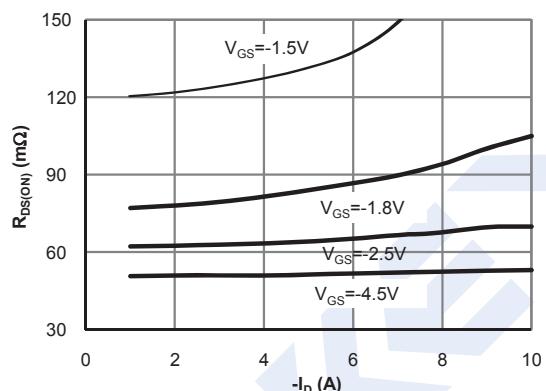
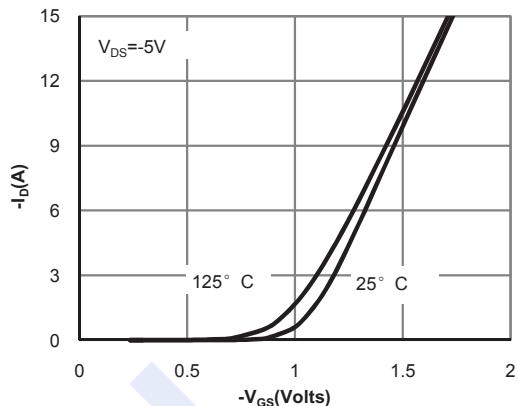
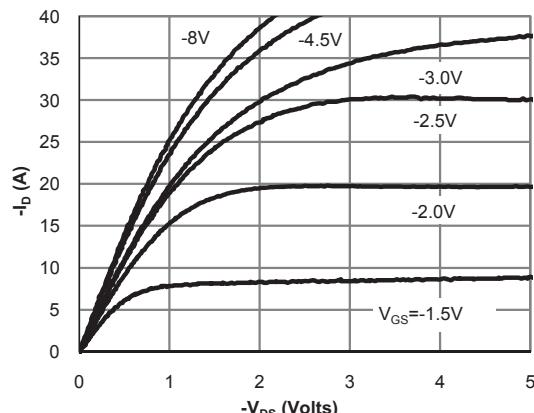
■ Marking

Marking	3415T
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P-Channel MOSFET

KI015P

■ Typical Characteristics



P-Channel MOSFET

KI015P

■ Typical Characteristics

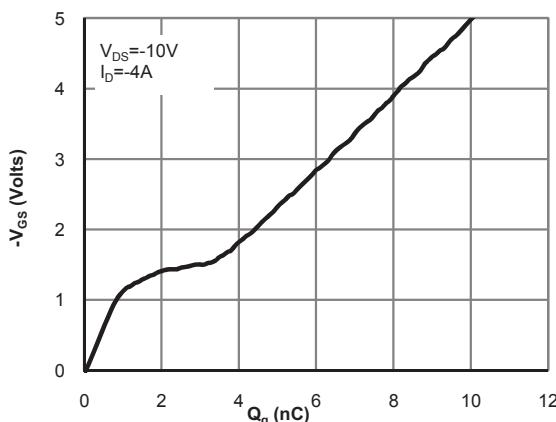


Figure 7: Gate-Charge Characteristics

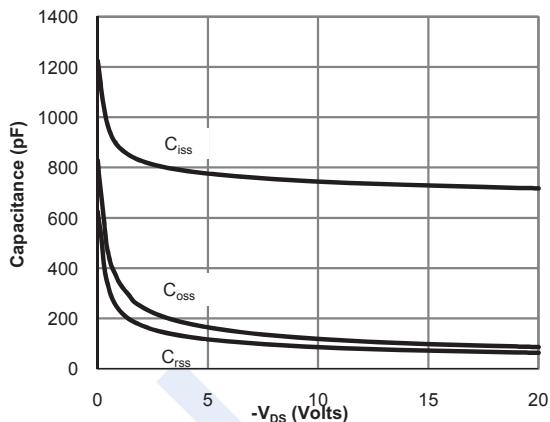


Figure 8: Capacitance Characteristics

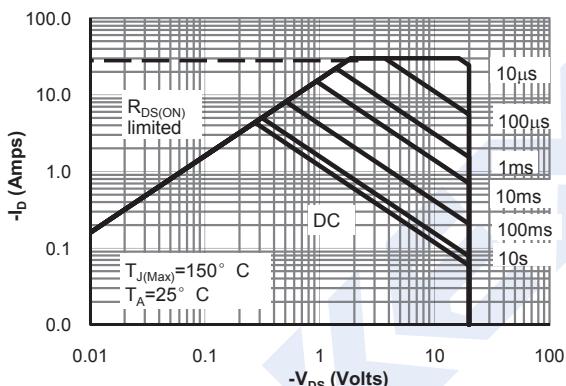


Figure 9: Maximum Forward Biased Safe Operating Area

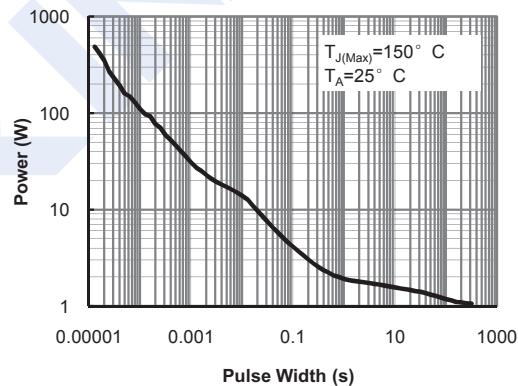


Figure 10: Single Pulse Power Rating Junction-to-Ambient

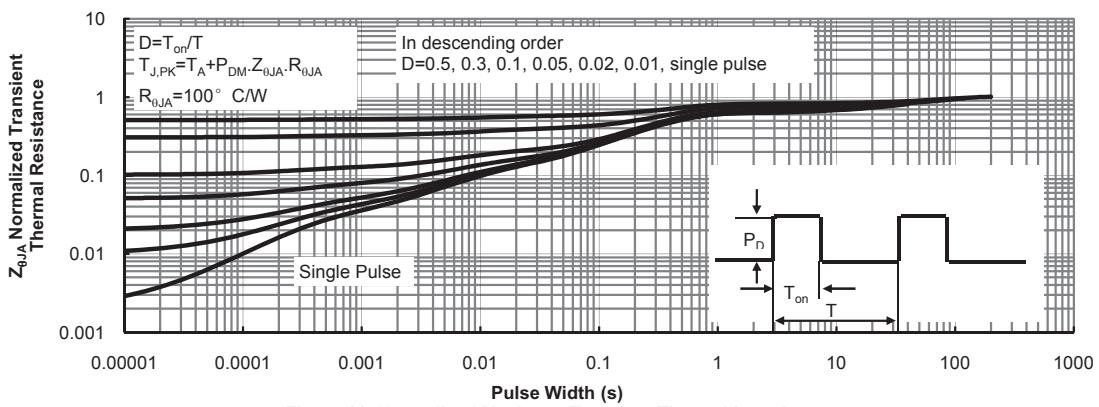


Figure 11: Normalized Maximum Transient Thermal Impedance