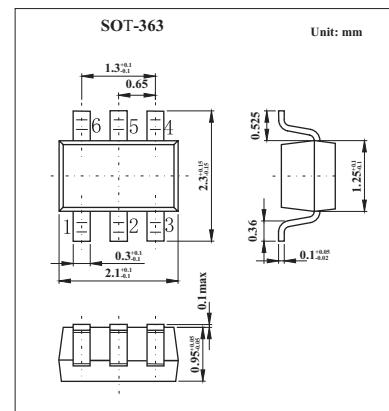
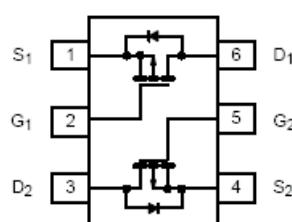


**Dual P-Channel 1.8-V (G-S) MOSFET****KI1905DL****■ Features**

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**■ Absolute Maximum Ratings Ta = 25°C**

Parameter	Symbol	5 secs	Steady State	Unit
Drain-source voltage	V <sub>DS</sub>		-8	V
Gate-source voltage	V <sub>GSS</sub>		±8	V
Continuous drain current (T <sub>J</sub> = 150°C)* TA=25°C TA=85°C	I <sub>D</sub>	± 0.60 ± 0.43	± 0.57 ± 0.41	A
Pulsed drain current	I <sub>DM</sub>		± 1.0	A
Continuous source current (diode conduction) *	I <sub>S</sub>	-0.25	-0.23	A
Power dissipation * TA=25°C TA=85°C	P <sub>D</sub>	0.30 0.16	0.27 0.14	W
Operating junction and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>		-55 to +150	°C

\* Surface Mounted on 1" X 1" FR4 Board.

**■ Thermal Resistance Ratings Ta = 25°C**

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient*	t ≤ 5 sec	R <sub>thJA</sub>	360	415
	Steady State		400	460
Maximum Junction-to-Foot (Drain)	R <sub>thJF</sub>	300	350	°C/W

\* Surface Mounted on 1" X 1" FR4 Board.

**KI1905DL**■ Electrical Characteristics  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Gate threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = -250 \mu\text{A}$	-0.45			V
Gate-body leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}$ , $V_{GS} = \pm 8 \text{ V}$			$\pm 100$	nA
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = -6.4 \text{ V}$ , $V_{GS} = 0 \text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -6.4 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $T_J = 85^\circ\text{C}$			-5	
On-state drain current	$I_{D(\text{on})}$	$V_{DS} = -5 \text{ V}$ , $V_{GS} = -4.5 \text{ V}$	-1.0			A
Drain-source on-state resistance	$r_{DS(on)}$	$V_{GS} = -4.5 \text{ V}$ , $I_D = -0.57 \text{ A}$		0.51	0.600	$\Omega$
		$V_{GS} = -2.5 \text{ V}$ , $I_D = -0.48 \text{ A}$		0.72	0.85	
		$V_{GS} = -1.8 \text{ V}$ , $I_D = -0.20 \text{ A}$		1.0	1.2	
Forward transconductance	$g_{fs}$	$V_{DS} = -10 \text{ V}$ , $I_D = -0.57 \text{ A}$		1.2		S
Diode forward voltage	$V_{SD}$	$I_S = -0.23 \text{ A}$ , $V_{GS} = 0 \text{ V}$		-0.8	-1.2	V
Total gate charge *	$Q_g$	$V_{DS} = -4 \text{ V}$ , $V_{GS} = -4.5 \text{ V}$ , $I_D = -0.57 \text{ A}$		1.5	2.3	nC
Gate-source charge *	$Q_{gs}$			0.17		
Gate-drain charge *	$Q_{gd}$			0.16		
Turn-on time	$t_{d(on)}$	$V_{DD} = -4 \text{ V}$ , $R_L = 8 \Omega$ , $I_D = -0.5 \text{ A}$ , $V_{GEN} = -4.5 \text{ V}$ , $R_G = 6 \Omega$		6	12	ns
	$t_r$			25	50	
Turn-off time	$t_{d(off)}$			10	20	
	$t_f$			10	20	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = -0.23 \text{ A}$ , $dI/dt = 100 \text{ A}/\mu\text{s}$		20	40	

\* Pulse test:  $PW \leq 300 \mu\text{s}$  duty cycle  $\leq 2\%$ .

## ■ Marking

Marking	QB
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