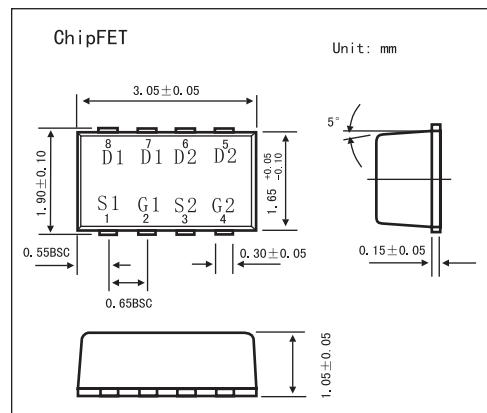
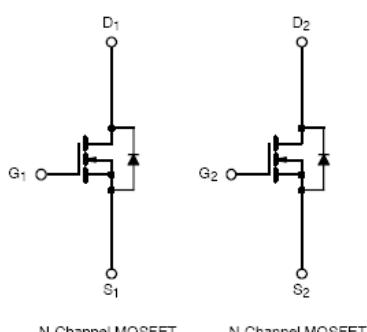


Dual N-Channel 2.5-V (G-S) MOSFET

KI5904DC

■ Features

-



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	5secs	Steady State	Unit
Drain-Source Voltage	V _{DS}	20		V
Gate-Source Voltage	V _{GS}	±12		
Continuous Drain Current (T _J = 150 °C) TA = 25°C	I _D	±4.2	±3.1	A
TA = 85°C		±3.0	±2.2	
Pulsed Drain Current	I _{DM}	±10		A
Continuous Source Current (Diode Conduction)*	I _S	1.8	0.9	
Maximum Power Dissipation * TA = 25°C	P _D	2.1	1.1	W
TA = 85°C		1.1	0.6	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150		°C
Soldering Recommendations		260		°C

*Surface Mounted on 1" X 1" FR4 Board.

■ Thermal Resistance Ratings

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient *	t ≤ 5 sec	R _{thJA}	50	°C/W
	Steady-State		90	
Maximum Junction-to-Foot (Drain)	Steady-State	R _{thJF}	30	40

* Surface Mounted on 1" X 1" FR4 Board.

KI5904DC

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Gate Threshold Voltage	V _{GS(th)}	V _{Ds} = V _{GS} , I _D = 250 μ A	0.6			V
Gate-Body Leakage	I _{GSS}	V _{Ds} = 0 V, V _{GS} = ±12 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{Ds} = 16V, V _{GS} = 0 V		1		μ A
		V _{Ds} = 16 V, V _{GS} = 0 V, T _J = 85°C		5		
On-State Drain Current*	I _{D(on)}	V _{Ds} ≥ 5 V, V _{GS} = 4.5 V	10			A
Drain Source On State Resistance*	R _{Ds(on)}	V _{GS} = 4.5 V, I _D = 3.1 A		0.065	0.075	Ω
		V _{GS} = 2.5V, I _D = 2.3A		0.115	0.143	
Forward Transconductanceb	g _f s	V _{Ds} = 10 V, I _D = 3.1 A		8		S
Schottky Diode Forward Voltage*	V _{SD}	I _S = 0.9 A, V _{GS} = 0 V		0.8	1.2	V
Total Gate Charge	Q _g	V _{Ds} = 10 V, V _{GS} = 4.5V, I _D = 3.1 A		4	7.5	nC
Gate-Source Charge	Q _{gs}			0.6		
Gate-Drain Charge	Q _{gd}			1.3		
Turn-On Delay Time	t _{d(on)}	V _{DD} =10V,R _L =10Ω ,I _D =1A,V _{GEN} =10V,R _G =6Ω		12	18	ns
Rise Time	t _r			35	55	
Turn-Off Delay Time	t _{d(off)}			19	30	
Fall Time	t _f			9	15	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 0.9 A, d _i /d _t = 100 A/ μ s		40	80	ns

* Pulse test :Pulse width ≤300 μ s,duty cycle≤2%