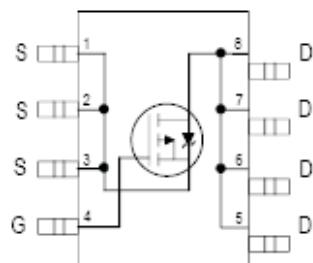
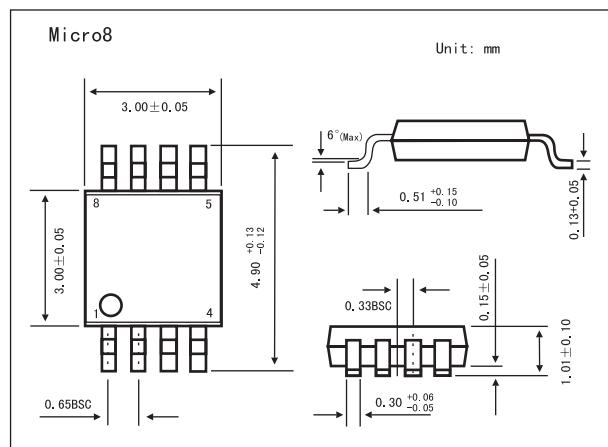


## HEXFET® Power MOSFET

### KRF7604

#### ■ Features

- Ultra Low On-Resistance
- P-Channel MOSFET
- Very Small SOIC Package
- Low Profile (< 1.1mm)
- Available in Tape & Reel
- Fast Switching



#### ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Continuous Drain Current, V <sub>GS</sub> @ -4.5V @ T <sub>a</sub> = 25°C	I <sub>D</sub>	-3.6	A
Continuous Drain Current, V <sub>GS</sub> @ -4.5V @ T <sub>a</sub> = 70°C	I <sub>D</sub>	-2.9	
Pulsed Drain Current *1	I <sub>DM</sub>	-19	
Power Dissipation @T <sub>a</sub> = 25°C	P <sub>D</sub>	1.8	W
Linear Derating Factor		14	mW/°C
Gate-to-Source Voltage	V <sub>GS</sub>	±12	V
Peak Diode Recovery dv/dt *2	dv/dt	-5.0	V/ns
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to + 150	°C
Maximum Junction-to-Ambient *3	R <sub>θ JA</sub>	70	°C/W

\*1 Repetitive rating; pulse width limited by max. junction temperature.

\*2 I<sub>SD</sub> ≤ -2.4A, dI/dt ≤ -96A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>, T<sub>J</sub> ≤ 150°C

\*3 Surface mounted on FR-4 board, t ≤ 10sec.

**KRF7604**

## ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250 μ A	-20			V
Breakdown Voltage Temp. Coefficient	△V <sub>(BR)DSS</sub> /△T <sub>J</sub>	I <sub>D</sub> = -1mA, Reference to 25°C		-0.022		V/°C
Static Drain-to-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.4A*1			0.09	mΩ
		V <sub>GS</sub> = -2.7V, I <sub>D</sub> = -1.2A*1			0.13	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μ A	-0.70			V
Forward Transconductance	g <sub>fS</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -1.2A*1	2.6			S
Drain-to-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V			-1.0	μ A
		V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C			-25	
Gate-to-Source Forward Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = -12V			-100	nA
Gate-to-Source Reverse Leakage		V <sub>GS</sub> = 12V			100	
Total Gate Charge	Q <sub>g</sub>	I <sub>D</sub> = -2.4A		13	20	nC
Gate-to-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = -16V		2.6	3.9	
Gate-to-Drain ("Miller") Charge	Q <sub>gd</sub>	V <sub>GS</sub> = -4.5V		5.6	9.0	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -10V		17		ns
Rise Time	t <sub>r</sub>	I <sub>D</sub> = -2.4A		53		
Turn-Off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 6 Ω		31		
Fall Time	t <sub>f</sub>	R <sub>D</sub> =4.0 Ω		38		
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V		590		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -15V		330		
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		170		
Continuous Source Current (Body Diode)	I <sub>s</sub>	MOSFET symbol showing the integral reverse p-n junction diode.			-1.8	A
Pulsed Source Current (Body Diode) *2	I <sub>SM</sub>				-19	
Diode Forward Voltage	V <sub>SD</sub>	T <sub>J</sub> = 25°C, I <sub>S</sub> = -2.4A, V <sub>GS</sub> = 0V*1			-1.2	V
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = -2.4A		41	62	ns
Reverse RecoveryCharge	Q <sub>rr</sub>	di/dt = 100A/ μ s*1		38	57	μ C

\*1 Pulse width ≤ 300 μ s; duty cycle ≤ 2%.

\*2 Repetitive rating; pulse width limited by max. junction temperature.