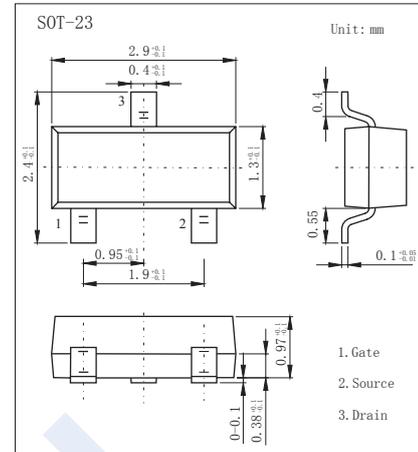
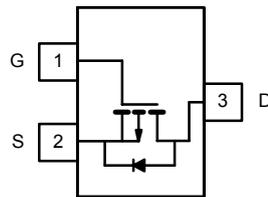


## P-Channel Enhancement MOSFET

### SI2335DS (KI2335DS)

#### ■ Features

- $V_{DS} (V) = -12V$
- $I_D = -4.0A (V_{GS} = -4.5V)$
- $R_{DS(ON)} < 51m\Omega (V_{GS} = -4.5V)$
- $R_{DS(ON)} < 70m\Omega (V_{GS} = -2.5V)$
- $R_{DS(ON)} < 106m\Omega (V_{GS} = -1.8V)$



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	5 sec	Steady State	Unit
Drain-Source Voltage	$V_{DS}$	-12		V
Gate-Source Voltage	$V_{GS}$	$\pm 8$		
Continuous Drain Current $T_a = 25^\circ C$ $T_a = 70^\circ C$	$I_D$	-4.0	-3.2	A
		-3.3	-2.6	
Pulsed Drain Current	$I_{DM}$	-15		
Power Dissipation $T_a = 25^\circ C$ $T_a = 70^\circ C$	$P_D$	1.25	0.75	W
		0.8	0.48	
Thermal Resistance.Junction- to-Ambient $t \leq 5$ sec Steady State	$R_{thJA}$	100		$^\circ C/W$
		166		
Thermal Resistance.Junction- to-Foot	$R_{thJF}$	50		
Junction Temperature	$T_J$	150		$^\circ C$
Storage Temperature Range	$T_{stg}$	-55 to 150		

## P-Channel Enhancement MOSFET

### SI2335DS (KI2335DS)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =-250 μA, V <sub>GS</sub> =0V	-12			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>D</sub> =-9.6V, V <sub>GS</sub> =0V			-1	μA
		V <sub>D</sub> =-9.6V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			-10	
Gate-Body leakage current	I <sub>GSS</sub>	V <sub>D</sub> =0V, V <sub>GS</sub> =±8V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>D</sub> =V <sub>GS</sub> I <sub>D</sub> =-250 μA	-0.45		-1.0	V
Static Drain-Source On-Resistance *1	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.0A		45	51	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3.5A		58	70	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-2A		82	106	
On state drain current *1	I <sub>D(ON)</sub>	V <sub>GS</sub> =-4.5V, V <sub>D</sub> =-5V	-15			A
		V <sub>GS</sub> =-2.5V, V <sub>D</sub> =-5V	-6			
Forward Transconductance *1	g <sub>FS</sub>	V <sub>D</sub> =-5V, I <sub>D</sub> =-4.0A		7		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>D</sub> =-6V, f=1MHz		1225		pF
Output Capacitance	C <sub>oss</sub>			260		
Reverse Transfer Capacitance	C <sub>rss</sub>			130		
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =-4.5V, V <sub>D</sub> =-6V, I <sub>D</sub> =-4.0A		9	15	nC
Gate Source Charge	Q <sub>gs</sub>			1.9		
Gate Drain Charge	Q <sub>gd</sub>			1.5		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =-4.5V, V <sub>D</sub> =-6V, R <sub>L</sub> =6 Ω, R <sub>GEN</sub> =6 Ω  I <sub>D</sub> =-1.0A		13	20	ns
Turn-On Rise Time	t <sub>r</sub>			15	25	
Turn-Off DelayTime	t <sub>d(off)</sub>			50	70	
Turn-Off Fall Time	t <sub>f</sub>			19	35	
Maximum Body-Diode Continuous Current	I <sub>S</sub>				-1.6	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1.6A, V <sub>GS</sub> =0V			-1.2	V

\*1Pulse test: PW ≤ 300us duty cycle ≤ 2%.

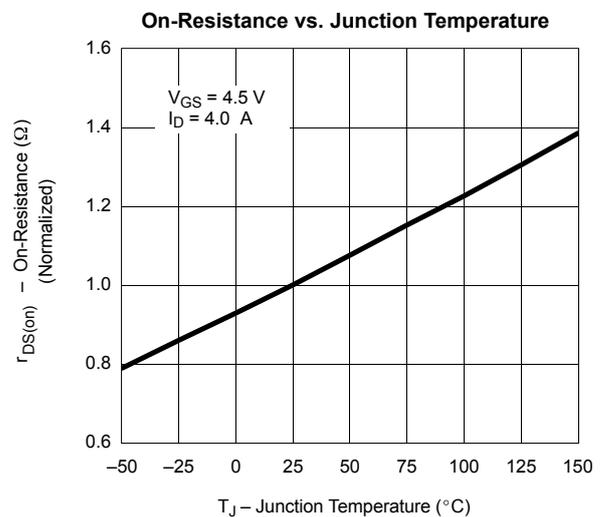
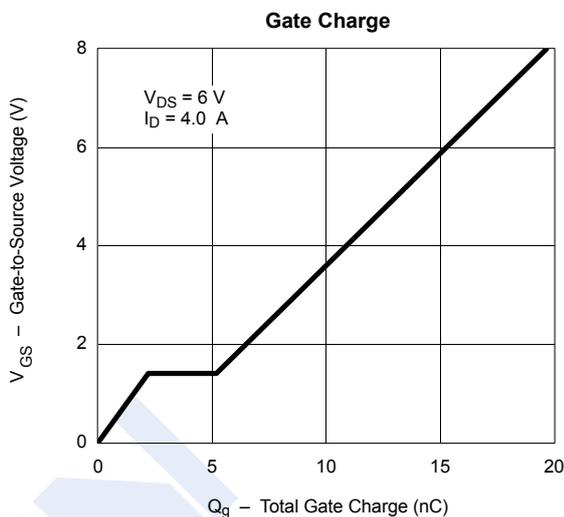
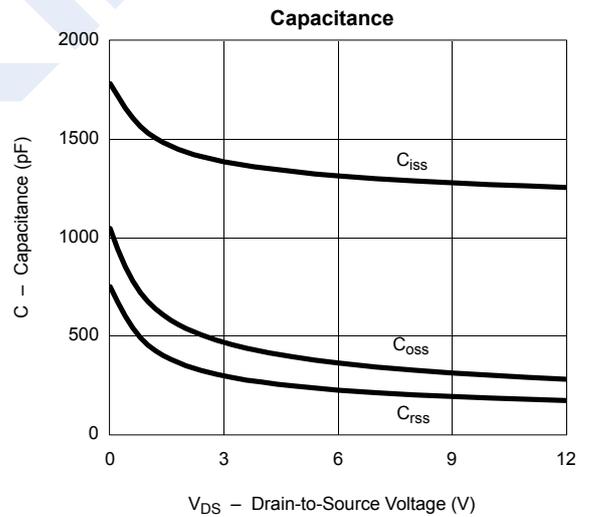
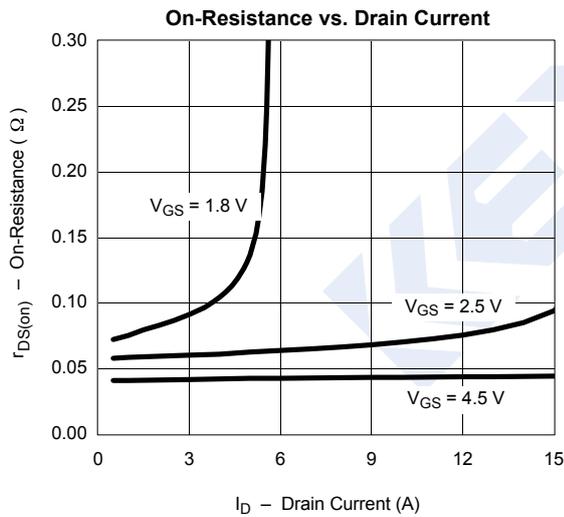
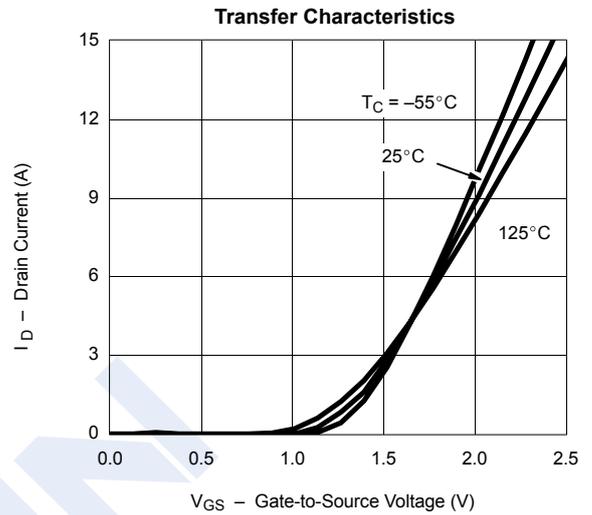
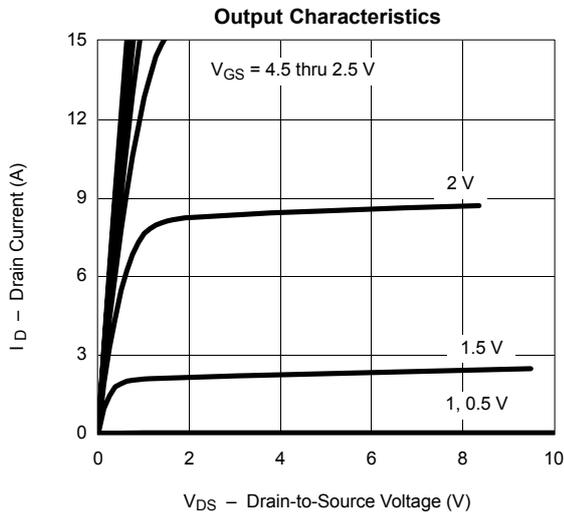
#### ■ Marking

Marking	E5*
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## P-Channel Enhancement MOSFET

### SI2335DS (KI2335DS)

#### ■ Typical Characteristics



# P-Channel Enhancement MOSFET

## SI2335DS (KI2335DS)

### Typical Characteristics

